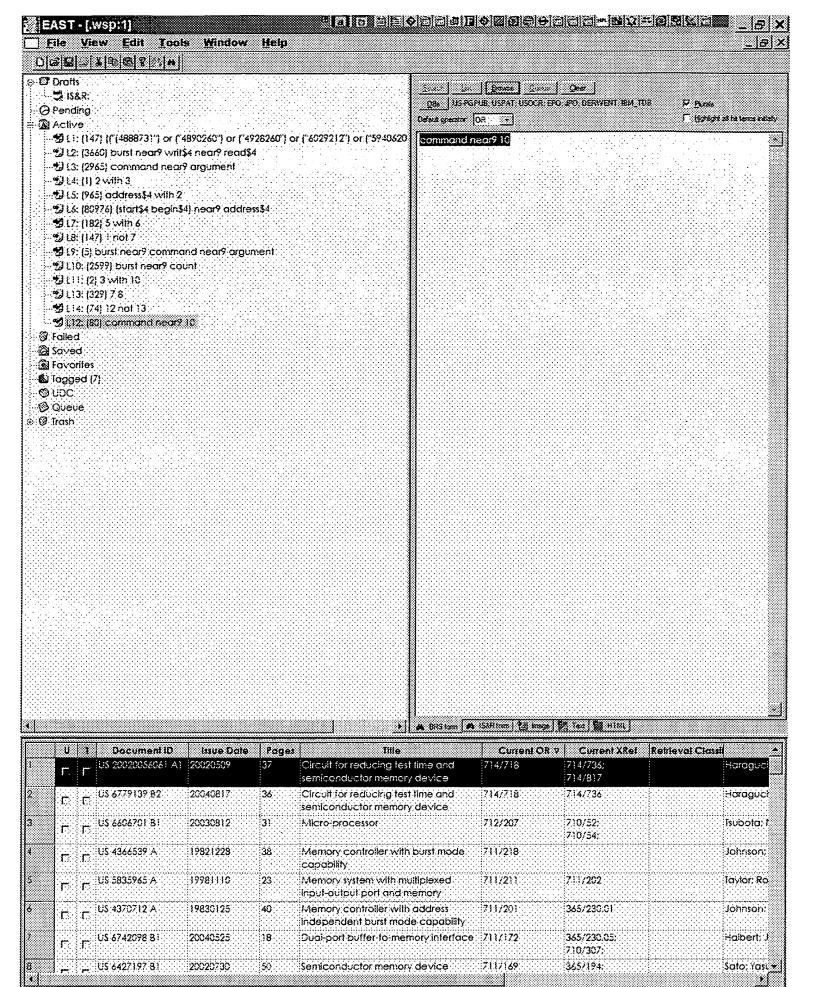
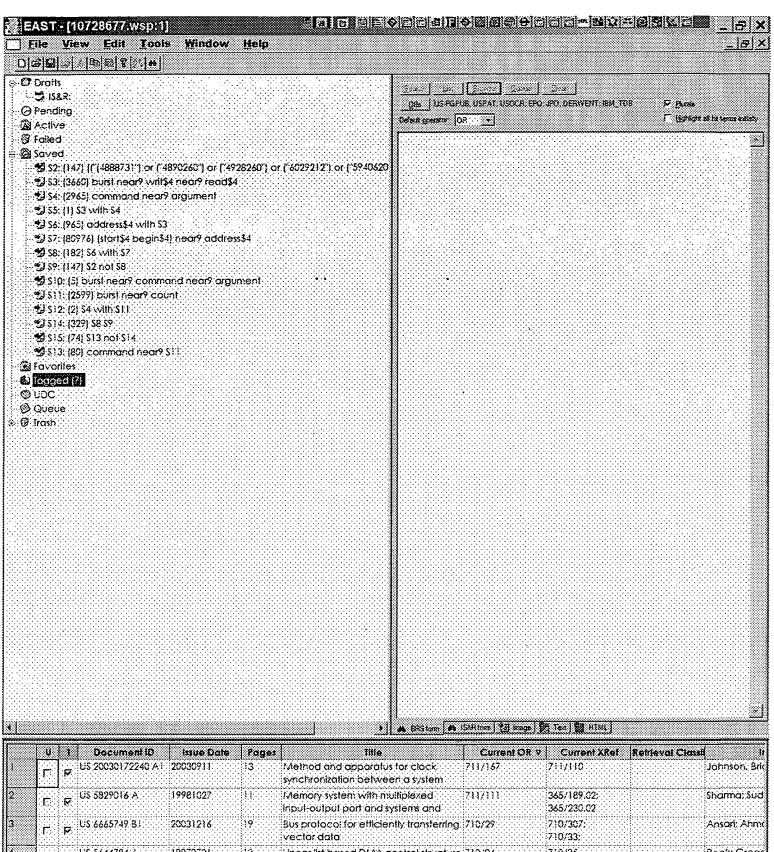


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	u	1	Document ID	issue Date	Pages	Title	Current OR V	Current XRef	Retrieval Classif	lt lt
	Γ	₽	US 20030172240 A1	20030911	13	Method and apparatus for clock synchronization behween a system	711/167	711/110		Johnson, Brid
2	Г	₽	US 5829016 A	19981G27	11	Memory system with multiplexed Input-output part and systems and	711/111	365/189.02; 365/230.02		Sharma; Sud
3	r	P	US 6665749 BI	20031216	JĢ	Bus protocol for efficiently transferring vector data	710/29	710/307; 710/33;		Ansari: Ahmi
•	г	7	US 5644784.A	19970701	13	Unear list based DMA control structure	710/24	710/35		Peek: Grego
5	r	Þ	US 5765022 A	19980609		System for transferring dafa from a source device to a target device in	710/22	7 0/26; 7 0/33;		Kalser; John
8	E	₽	US 6229759 B1	20010508		Semiconductor memory burst length count determination method	365/236	365/189:05; 365/233;		Morgan; Do
	Γ	₽	US 5347634 A	19940913	12	System and method for directly executing user DMA instruction from	345/539	222/500		Herrell: Russ
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